



S-03-005A

February 2, 2005

To: Commissioner for Patents  
P.O.Box 1450  
Alexandria, VA 22313-1450

Fr: George O. Saile, Reg. No. 19,572  
28 Davis Avenue  
Poughkeepsie, N.Y. 12603

Subject: | Serial No. 10/764,920 01/26/04 |  
Andreas Sibrai et al.  
HIGH Q LINEAR CONTROLLED VARIABLE  
CAPACITOR  
| \_\_\_\_\_ |

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Enclosed is Form PTO-1449, Information Disclosure Citation  
In An Application.

The following Patents and/or Publications are submitted to  
comply with the duty of disclosure under CFR 1.97-1.99 and  
37 CFR 1.56.

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being  
deposited with the United States Postal Service as first class  
mail in an envelope addressed to: Commissioner for Patents,  
P.O. Box 1450, Alexandria, VA 22313-1450, on February 7, 2005.

Stephen B. Ackerman, Reg.# 37761

Signature/Date

832 2/7/05

DS-03-005A

A translation of the abstract of the Patent Abstracts of Japan is attached:

Patent Abstracts of Japan JP 62076801 to Nishihara Toshiyuki, "Digital Temperature Compensation Crystal Oscillator," discusses improving the C/N and to make the titled oscillator suitable for large scale circuit integration by allowing each charge/discharge circuit to output an analog signal while the impedance of each transistor (TR) switch element is changed timewise consecutively.

European Patent Application EP 0 431 887 A to Yoichi, Imamura, "Variable Capacitance Capacitor Array," discloses a capacitor array arranged for providing a variable capacitance, and particularly a high accuracy temperature compensating liquid crystal oscillator circuit having such a capacitor array for adjusting the frequency of the oscillator output.

International Patent WO 01/06637 A to Collier et al., "Adjustable Filter," discusses the adjustment of filters, especially in ways that can address manufacturing variations.

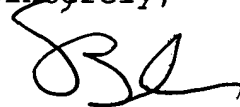
U.S. Patent 5,235,335 to Hester et al., "Circuit and Method for Tuning Capacitor Arrays," discloses a capacitor array circuit.

"RF-CMOS Oscillators with Switched Tuning," by Kral et al., IEEE 1998 Custom Integrated Circuits Conference, pp. 555-558, addresses the practical problem of how to design RF CMOS oscillators with a wide enough tuning range to reliably cover process variations, without compromising current drain or phase noise.

U.S. Patent 6,476,682 to Cole et al., "Method for Calibrating a Temperature Sensitive Oscillator," discloses a temperature compensated crystal oscillation circuit adapted to be contained within a small device package and providing an output frequency accuracy of approximately +/-2 ppm over a temperature range or less than minutes per year over the temperature range.

U.S. Patent Application Publication US 2003/0045069 A1 to Gilgen et al., "Capacitor for Use in an Integrated Circuit," discusses a capacitor including a first plate of conductive material that is formed in a predetermined shape.

Sincerely,

A handwritten signature in black ink, appearing to read 'SBA', is written over the word 'Sincerely,'.

Stephen B. Ackerman,  
Reg. No. 37761

# INFORMATION DISCLOSURE CITATION IN AN APPLICATION

(Use several sheets if necessary)

Docket Number (Optional)

DS-03-005A

Application Number

10/764,920

Applicant

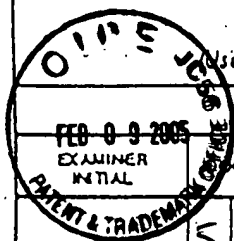
Andreas Sibrai et al.

Filing Date

01/26/04

Group Art Unit

## U. S. PATENT DOCUMENTS



DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILED DATE IF APPROPRIATE
5235335	8/10/93	Hester et al.	341	172	6/2/92
6476682	11/5/02	Cole et al.	331	176	10/22/99

## FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
					YES	NO
JP 6 2076801	4/8/87	Japan	H03B	5/32		
EP 0 431 887 A	6/12/91	European Patent	H03L	1/02		
WO 01 / 06637 A	1/25/01	International Patent	H03B	5/12		

## OTHER DOCUMENTS (Including Author, Title, Date, Portion of Pages, Etc.)

-	"RF-CMOS Oscillators with Switched Tuning", by Kral et al., IEEE 1998 Custom Integrated Circuits Conf., pp. 555-558.
-	U.S. Patent App. Pub. US 2003/0045069 A1 to Gilgen et al., Pub. Date 3/6/03, Filed 8/30/01, U.S. Cl. 438/396.

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.